BC807-16LT1, BC807-25LT1, BC807-40LT1

General Purpose Transistors

PNP Silicon

Features

• Pb–Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	-45	V
Collector - Base Voltage	V _{CBO}	-50	V
Emitter - Base Voltage	V _{EBO}	-5.0	V
Collector Current – Continuous	Ι _C	-500	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR-5 Board, (Note 1) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

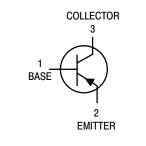
1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.

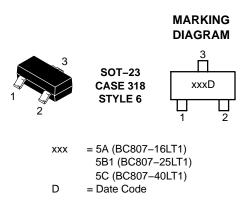
2. Alumina = 0.4 x 0.3 x 0.024 in 99.5% alumina.



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ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

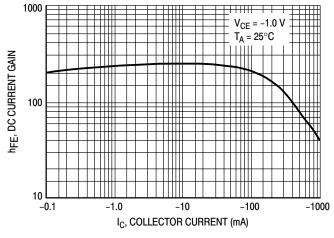
BC807-16LT1, BC807-25LT1, BC807-40LT1

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted.)

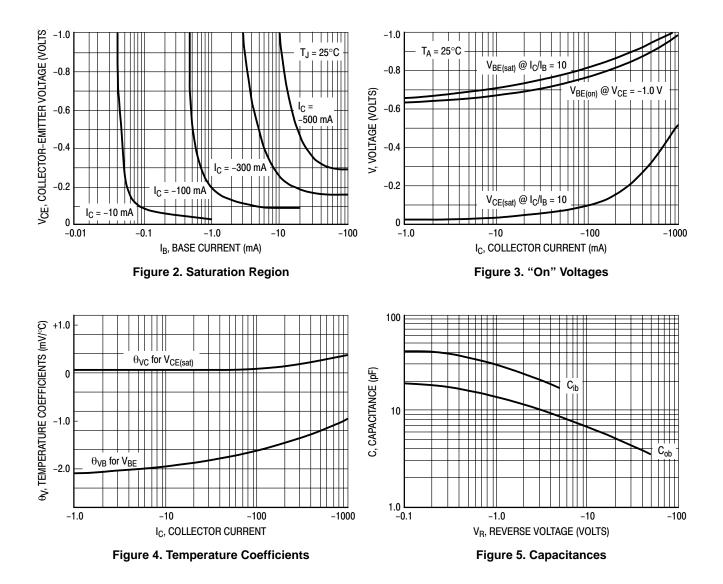
Cha	Symbol	Min	Тур	Мах	Unit	
OFF CHARACTERISTICS			•	•	•	•
Collector – Emitter Breakdown Volta ($I_C = -10 \text{ mA}$)	ge	V _{(BR)CEO}	-45	-	-	V
Collector – Emitter Breakdown Voltage ($V_{EB} = 0, I_C = -10 \mu A$)		V _{(BR)CES}	-50	-	-	V
Emitter – Base Breakdown Voltage $(I_E = -1.0 \ \mu A)$		V _{(BR)EBO}	-5.0	-	_	V
Collector Cutoff Current ($V_{CB} = -20 V$) ($V_{CB} = -20 V$, $T_J = 150^{\circ}C$)		I _{CBO}			-100 -5.0	nA μA
ON CHARACTERISTICS						
DC Current Gain (I _C = -100 mA, V _{CE} = -1.0 V) (I _C = -500 mA, V _{CE} = -1.0 V)	BC807–16 BC807–25 BC807–40	h _{FE}	100 160 250 40		250 400 600 -	-
Collector – Emitter Saturation Voltage ($I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$)	ge	V _{CE(sat)}	_	-	-0.7	V
Base – Emitter On Voltage ($I_C = -500 \text{ mA}, I_B = -1.0 \text{ V}$)		V _{BE(on)}	-	-	-1.2	V
SMALL-SIGNAL CHARACTER	ISTICS					
Current-Gain – Bandwidth Produc ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f =		f _T	100	-	-	MHz
Output Capacitance $(V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz})$		C _{obo}	Ι	10	-0.7	pF
DEVICE ORDERING INFORMA	TION					
Device	Package		Shipping [†]			
BC807-16LT1	SOT-23		3,000 Tape & Reel			
BC807-16LT3	SOT-23		10,000 Tape & Reel			
BC807-25LT1	SOT-23					
BC807-25LT1G	SOT-23 (Pb-Free)		3,000 Tape & Reel			
BC807-25LT3	SOT-23		10,000 Tape & Reel			
BC807-40LT1	SOT-23		, , , , , , , , , , , , , , , , , 			
BC807-40LT1G	SOT-23 (Pb-Free)	SOT-23		3,000 Tape & Reel		
BC807-40LT3	SOT-23					
BC807-40LT3G	SOT-23 (Pb-Free)	SOT-23		10,000 Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BC807-16LT1, BC807-25LT1, BC807-40LT1

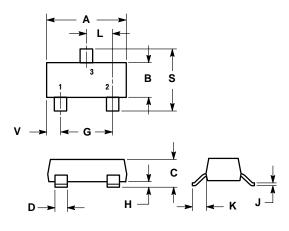






PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-09 ISSUE AI



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

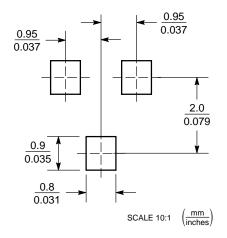
- CONTROLLING DIMENSION: INCH.
 MAXIUMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL. 4. 318-01, -02, AND -06 OBSOLETE, NEW STANDARD 318-09.

	INCHES		MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0385	0.0498	0.99	1.26
D	0.0140	0.0200	0.36	0.50
G	0.0670	0.0826	1.70	2.10
н	0.0040	0.0098	0.10	0.25
J	0.0034	0.0070	0.085	0.177
к	0.0180	0.0236	0.45	0.60
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.0984	2.10	2.50
v	0.0177	0.0236	0.45	0.60

STYLE 6: PIN 1. BASE 2. EMIT

2. EMITTER
 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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