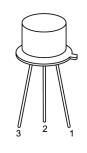
# **Bipolar Transistor**

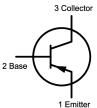




## RoHS Compliant

**NPN** 





### **Description:**

This is a silicon NPN transistor in a TO-18 type case designed primarily for amplifier and switching applications. The device features high breakdown voltage, Low leakage current, low capacity, and beta useful over an extremely wide current range.

#### Pin Configuration:

- 1. Emitter
- 2. Base
- 3. Collector

### **Maximum Ratings:**

Characteristic	Symbol	Rating	Unit	
Collector-Base Voltage	V <sub>CBO</sub>	140		
Collector-Emitter Voltage	V <sub>CEO</sub>	80	V	
Emitter Base Voltage	V <sub>EBO</sub>	7		
Continuous Collector Current	I <sub>C</sub>	1	A	
Total Device Dissipation -(T <sub>A</sub> = +25°C), Derate Above 25°C	В	0.5 2.85	W	
Total Device Dissipation -(T <sub>A</sub> = +25°C), Derate Above 25°C	- P <sub>D</sub>	1.8 10.6	mW/°C	
Operating Junction Temperature Range	T <sub>J</sub>	05 to 1000	°C	
Storage Temperature Range,	T <sub>stg</sub>	-65 to +200		
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	97	°C/W	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	350		
Lead Temperature (During Soldering, 1/16" from case, 60sec max)	T <sub>L</sub>	300	°C	

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# **Bipolar Transistor**



#### **Electrical Characteristics:** (TA = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Max.	Unit
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	$I_{\rm C} = 30 {\rm mA}, I_{\rm B} = 0$	80		
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> = 100μA. I <sub>E</sub> = 0	140	-	V
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 100μA. I <sub>C</sub> = 0	7		
Collector Cut-Off Current	I <sub>CBO</sub>	$V_{CB} = 90V, I_{E} = 0$		0.01	
Collector Cut-Oil Current		$V_{CB} = 90V, I_{E} = 0, T_{A} = +150^{\circ}C$	-	10	μA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>BE</sub> = 5V, lc = 0		0.01	Α

#### **ON Characteristics**

	1				
	h <sub>FE</sub>	$V_{CE} = 10V, I_{C} = 0.1 \text{mA}$	50		
		$V_{CE} = 10V, I_{C} = 10mA$	90	_	
DC Current Gain (Note 1)		V <sub>CE</sub> = 10V, I <sub>C</sub> = 150mA	100	300	
		$V_{CE} = 10V, I_{C} = 150mA, TA = -55^{\circ}C$	40		-
		V <sub>CE</sub> = 10V, I <sub>C</sub> = 500mA	50	-	
		V <sub>CE</sub> = 10V, I <sub>C</sub> = 1A	15		
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA		0.2	
Collector-Emitter Saturation Voltage		$I_{\rm C} = 500  \rm mA, I_{\rm B} = 50  \rm mA$	-	0.5	V
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA		1.1	

### **Small - Signal Characteristics**

Current Gain-Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 20MHz	100	400	MHz
Output Capacitance	C <sub>obo</sub>	V <sub>CB</sub> = 10V, I <sub>E</sub> = 0, f = 1MHz		12	pF
Input Capacitance	C <sub>lbo</sub>	$V_{BE} = 500 \text{mV}, I_{C} = 0, f = 1 \text{MHz}$	-	60	ρг
Small-Signal Current Gain	h <sub>fe</sub>	$V_{CE} = 5V$ , $I_C = 1mA$ , $f = 1kHz$	80	400	-
Collector-Base Time Constant	rb'C <sub>c</sub>	V <sub>CB</sub> = 10V, I <sub>E</sub> = 10mA, f = 79.8MHz		400	ps
Noise Figure	NF	$V_{CE} = 10V, I_{C} - 100\mu A. f = 1kHz, R_{S} = 1k\Omega$	-	4	dB

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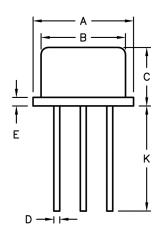
#### Note:

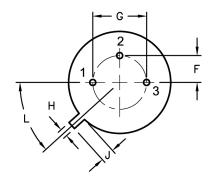
1. Pulse Test : Pulse Width ≦300µs, Duty Cycle ≦2%



# **Bipolar Transistor**







#### Pin Configuration:

- 1. Emitter
- 2. Base
- 3. Collector

Dim	Α	В	С	D	E	F	G	Н	J	K	L
Min.	5.24	4.52	4.31	0.4	-	-	-	0.91	0.71	12.7	45°
Max.	5.84	4.97	5.33	0.53	0.76	1.27	2.97	1.17	1.21	-	45

Dimensions: Millimetres

#### **Part Number Table**

Description	Part Number		
Transistor, NPN, 1A, 80V, TO-18	2N3700		

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